## REMARKS

Claims 1-17, 21-27 and 31-36 have been canceled. Thus, Claims 18-20 and 28-30 are currently pending in the present application, all of which have been amended.

Claim 19 has been amended to limit the subject matter of Claim 18; thus, the claim objection is believed to be overcome.

Rejection under 35 U.S.C. § 102

Claims 18-20, 24-29 and 31-36 were rejected under 35 U.S.C. § 102(b) as being anticipated by *Requa et al.* (The Piecewise Data Flow Architecture: Architectural Concepts, IEEE transactions on Computers, May 1993). Applicants respectfully traverse such rejection insofar as it might apply to the claims as amended herein.

Amended Claim 1 (and similarly Claim 28) now recites a step of "assigning each of said instruction groups to a respective set of said interconnected computation nodes such that results yielded by an instruction within one of said instruction groups are sent directly to an instruction within another one of said instruction groups or an output operand associated with an instruction within one of said instruction groups is directly written to a register file to be read by an instruction within another one of said instruction groups as an input operand."

On page 426, first column, second paragraph, *Requa* discloses that instructions can be grouped into relatively small blocks, and that each block includes information on data dependencies within that block. However, *Requa* does not teach or suggest the claimed assigning step that allows "results yielded by an instruction within one of said instruction groups ... [to be] sent directly to an instruction within another one of said instruction groups or an output operand associated with an instruction within one of said instruction groups ... [to be] directly written to a register file to be read by an instruction within another one of said instruction groups as an input operand."
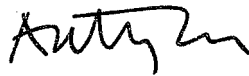
Because the claimed invention recites novel features that are not taught or suggested by the cited references, the § 102 rejection is believed to be overcome.

# CONCLUSION

Claims 18-20 and 28-30 are currently pending in the present application. For the reasons stated above, Applicants believe that independent Claims 18 and 28 along with their respective dependent claims are in condition for allowance. The remaining prior art cited by the Examiner, but not relied upon, has been reviewed and is not believed to show or suggest the claimed invention.

No fee or extension of time is believed to be necessary; however, in the event that any fee or extension of time is required for the prosecution of this application, please charge it against Dillon & Yudell Deposit Account No. **50-3083**.

Respectfully submitted,

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